

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



APPLICANT: JOSEPH MICHAEL HOKE ET AL

FOR: METHOD AND SYSTEM FOR SELECTING DATA SAMPLING PHASE FOR SELF
TIMED INTERFACE LOGIC

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §§ 1.56, 1.97 AND 1.98

The Commissioner of Patents
and Trademarks
Washington, D.C. 20231

Sir:

In compliance with the duty to disclose, applicants submit herewith a copy of each publication of which those designated by 37 CFR § 1.56 are aware; the publications are listed on attached form PTO-1449. The present Information Disclosure Statement shall not be construed as a representation that a search has been made or that no other art than that identified on the attached form PTO-1449 exists.

Consideration of this Information Disclosure Statement is respectfully requested, since the Examiner may consider the information provided herein material to the patentability of the subject application as defined in 37 CFR § 1.56.

Since this Information Disclosure Statement is being filed within three months of the filing date of the subject application, no fee or certification under 37 CFR § 1.97 (e) is required.

However, in the event the Commissioner of Patents deems that any fee is required under 37 CFR §§ 1.16 and 1.17 in connection with this application, applicants' attorneys authorize that such fee be charged to Deposit Account No. 09-0463.

Respectfully submitted,

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INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>				ATTY DOCKET NO. POU920010044US1		SERIAL NO.	
				JOSEPH MICHAEL HOKE ET AL			
				FILING		GROUP	

JCS 29 U.S. PTO
 06/910081
 APR 1991
 187/30/01

U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	
		5,287,020	02/15/94	Kimura et al			
		5,304,855	04/19/94	Naufel			
		5,487,095	01/23/96	Jordan et al			
		5,513,377	04/30/96	Capowski et al			
		5,519,737	05/21/96	Brun et al			
		5,522,088	05/28/96	Halma et al			
		5,535,377	07/09/96	Parks			
		5,568,526	10/22/96	Ferraiolo et al			
		5,598,442	01/28/97	Gregg et al			
		5,790,560	08/04/98	Durham et al			
		5,832,047	11/03/98	Ferraiolo et al			

FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
		EPO687986A2	12/20/95	EUROPE			✓	
		EPO687980A2	12/20/95	EUROPE			✓	
		EPO687982A1	12/20/95	EUROPE			✓	

OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>			
			J.J. Pertzborn, IBM TDB "Digital Clock Synchronization Circuit" , Vol. 22, Pages 311-314

EXAMINER	DATE CONSIDERED
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

